1. Introduction

The CaCu$_3$Ti$_4$O$_{12}$ (CCTO) compound has recently attracted considerable research interest due to its unusually high and weakly temperature dependent dielectric permittivity ($k \sim 10^3$–$10^5$). No structural phase transition is reported in CCTO in the temperature range 100–600 K and the material has been projected as a potential alternative for the currently used ferroelectrics for miniaturization of microelectronics (Ramirez et al 2000; Subramanian et al 2000; Homes et al 2001; Adams et al 2002; Cohen et al 2003; Lukenheimer et al 2004). CCTO has a distorted, complex cubic perovskite like structure with unit cell parameter, $a \sim 7.393$ Å (Bochu et al 1979). The unusual dielectric property of CCTO was first explained by Subramanian et al (2000) in terms of local dipole moments associated with off-centre displacement of Ti ions. An extrinsic explanation based on insulating twin boundaries through single crystal studies has been reported here. Impedance spectroscopic studies showed that CCTO is electrically heterogeneous with semiconducting grains and insulating grain boundaries and the high dielectric constant was attributed to internal barrier layer capacitance (IBLC) (Adams et al 2002). Lukenheimer et al (2004) explained the behaviour through interfacial electrode polarization due to the development of schottky barriers between the sample and electrode interface. Presence of schottky type grain boundaries within CCTO was confirmed using microcontact current–voltage measurements and Kelvin probe force microscopy (Chung et al 2004). Intrinsic explanations such as existence of highly polarizable relaxation modes (Ramirez et al 2000) and relaxor like slowing down of dipolar fluctuations in nano-size domains (Homes et al 2001) were reported to be the reason for the colossal dielectric constant (CDC). However, most of the recently reported studies supported that CDC is microstructural dependent and is due to the development of schottky barriers between grain/domain boundaries and the semiconducting grains (Adams et al 2006; Briže et al 2006; Liu et al 2007; Martin et al 2008; Sun et al 2008).

The thin film state of CCTO possesses more importance for realizing commercial application in integrated circuits. Besides pulsed-laser deposition (PLD) and metal organic chemical vapour deposition (MOCVD), chemical solution deposition (CSD) is an attractive and economic method for making oxide thin films with good homogeneity and composition control (Schwartz 1997). However, only a few studies were reported on CCTO thin films produced by CSD (Lu et al 2004; Feng et al 2006; Jiménez et al 2007). Also reports on dielectric properties of CCTO thin films produced by CSD are limited (Jiménez et al 2007).

In the present work, we report the deposition of CCTO thin film using chemical solution deposition, spin coating and rapid thermal processing. A new precursor solution was used for the purpose. The phase and microstructure of the prepared films were studied as a function of sintering temperature. Dielectric properties of the film were measured at room temperature as a function of sintering temperature. Dielectric properties of the film were measured at room temperature as a function of measuring frequency from 1 kHz–1 MHz.

2. Experimental

2.1 Thin film processing

The starting chemicals for the precursor solution were high purity calcium acetylacetonate (Ca(C$_3$H$_7$O$_2$)$_2$. 

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99.95%; Aldrich, USA), copper nitrate (\(\text{Cu(NO}_3\text{)}_2\cdot 3\text{H}_2\text{O}\), 99%; Junsei, Japan) and titanium butoxide (\(\text{Ti(O(CH}_3\text{)}_3\text{CH}_3\)\(\text{)}_4\), 97%, Sigma-Aldrich, USA). The precursor chemicals were weighed and mixed in the molar ratio of \(\text{Ca} : \text{Cu} : \text{Ti} = 1 : 3 : 4\). The solvent used was 2-methoxy ethanol (\(\text{C}_3\text{H}_8\text{O}_2\), 99.9%, Sigma-Aldrich, USA). The selection of the precursor chemicals was done with a view to favour maximum chelating effect. The total volume of the reagents to the solvent was kept in a ratio of 1 : 2. The mixed solution was subjected to stirring using a magnetic stirrer for 10 h. Spin coating was carried out at 3000 rpm for 20 s. The substrate used was commercially available heavily boron-doped silicon. The coated film was subjected to pyrolysis at a temperature of 500°C for 2 min in air. The heating rate used from room temperature to the pyrolysis temperature was \(~ \text{50°C/s}\) (time of heating, 10 s). The procedure of coating and pyrolysis was repeated two to three times in order to obtain a desired film thickness of \(~ \text{500 nm}\). Lastly the film was subjected to sintering at different temperatures ranging from 700–800°C for 8 min in air. A heating rate of \(~ \text{75°C/min}\) was used from the pyrolysis temperature to the sintering temperature (time of heating from 500–800°C, 4 min). The final pyrolysis and sintering were performed in a single step. Rapid thermal annealing (RTA, ULVAC-RIKO, MILA-3000) was employed. The cooling rate was \(~ \text{25°C/min}\). Selection of the pyrolysis and sintering temperature was in accordance with the TG/DTA studies of the dried precursor solution (not shown here). Selection of optimized temperature, time and heating rate for both pyrolysis and sintering was essential for getting pure phase CCTO. Figure 1 shows a flow chart of the film processing.

2.2 Phase and microstructure

The crystalline structure of sintered films was examined using X-ray diffraction (XRD, X’pert Pro, Philips) using CuKα radiation. Scanning electron microscope was used to study the film surface morphology as well as for film thickness measurements (FE–SEM, Hitachi 4800). Before SEM observation, thin gold film was deposited on the sample surface.

2.3 Dielectric properties

An impedance analyzer (HP 4194A, Hewlett-Packard) was used to measure the capacitance and dielectric loss of the film in the frequency range 1 kHz–1 MHz at room temperature, 25 ± 1°C. Dielectric constant was calculated from the obtained capacitance values and the film thickness. Top platinum electrodes with an area of \(5 \times 10^{-7} \text{m}^2\) were deposited by d.c. magnetron sputtering on the film surface using a shadow mask for the dielectric measurements. The time of platinum sputtering was 20 min.

3. Results and discussion

3.1 Phase and microstructure

Figure 2 represents the XRD patterns recorded for the CCTO films as a function of sintering temperature. At the temperature range of 600–700°C, CuO was the main constituent of the crystallized phase. Along with the CuO phase, anatase and rutile forms of TiO\(_2\) were detected. The unidentified peaks may correspond to titanium or...
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Figure 3. SEM images of CCTO thin films as a function of sintering temperature.

silicon based compounds. However, when the sintering temperature was increased to 750°C, the second phases disappeared and pure phase CCTO thin film was obtained. The film exhibited (220) preferential orientation. The diffraction patterns showed distinct (220), (400), (422) and (440) peaks along with low intensity peaks. The peaks match with the corresponding peaks in bulk CCTO, revealing polycrystalline nature of the film with cubic perovskite structure (space group, $\text{Im}3$) (Bochu et al 1979). With further increase in sintering temperature and sintering time, no major structural changes were observed.

Representative SEM surface and lateral images obtained for the CCTO films as a function of sintering temperature is shown in figure 3. The SEM images support the XRD results revealing polycrystalline nature. The film exhibited exaggerated grain growth and duplex microstructure consisting of large grains in excess of 400 nm in length and small grains of <200 nm. The exaggerated grain growth may be attributed to the CuO based liquid phase sintering (Leret et al 2007). Trapped pores were observed in large grains and in grain boundaries when the sintering temperature was above 900°C. The pores may have developed from the abnormal grain growth and the grain boundaries moving quickly during the sintering process (Liu et al 2007). Due to the exaggerated grain growth, any significant variation in grain growth with sintering temperature was not observed. Lateral view image in the figure corresponds to a three-time spin coated film sintered at 800°C. The image indicates a well densified structure with some columnar grains. The average thickness of a three-time spin coated film was ~400 nm. The interface between the CCTO film and bottom electrode was sharp implying negligible inter-diffusion across the interface.

3.2 Dielectric properties

The variation of $k$ and the dielectric loss recorded for the films as a function of frequency of measurement is shown in figure 4. The dielectric measurements were carried out in four selected frequencies, viz. 1 kHz, 100 kHz, 500 kHz and 1 MHz. The CCTO film sintered at 750°C showed a $k$ value of ~2100 and a loss tangent of ~0.06 at 1 kHz. The decrease in $k$ when sintering temperature was above 900°C is associated with the trapped porosity as observed by SEM. At higher measuring frequencies, $k$ decreased gradually, accompanied by a corresponding increase in loss factor.

The large $k$ at lower frequencies is suggested to be associated with the Maxwell-Wagner contributions of
depletion layers at grain boundaries or interfaces between the sample and contacts (Adams et al 2002; Lukenheimer et al 2004). So far, the internal barrier layer capacitor (IBLC) model has been widely accepted as the most likely mechanism to elucidate the CDC in CCTO. The difference of conductivity between grain boundary and grain volume causes the charge accumulation on the grain boundary on a mesoscopic scale, which results in large quantity boundary barrier layer capacitors. On a macroscopic scale, the electrode contact effect may play a significant role in CDC. Liu et al (2008) showed that electrode and grain boundary act as two depletion layers with distinct relaxation. On increasing the frequency, the space charge polarization relaxes leading to decrease in $k$. It has been shown that the frequency of dielectric relaxation was much lower in the case of CCTO film when compared to that of the bulk ceramic (Shri Prakash et al 2008). The dielectric properties of the film obtained in the present study is comparable with that reported for polycrystalline CCTO thin films (Si et al 2002; Fang and Shen 2003; Fang et al 2004; Shri Prakash et al 2008). The high dielectric constant obtained for the CCTO film in the present study indicates its possible application in microelectronic devices.

4. Conclusions

Highly oriented CCTO thin films were successfully deposited on boron doped silica substrate at a sintering temperature of 750°C. Effect of sintering temperature on the phase and microstructure of the film was investigated. The film formed was polycrystalline in nature with (220) preferential orientation. SEM microstructures of the films revealed a bimodal size distribution of grains. Dielectric constant and loss tangent at 1 kHz obtained for the film sintered at 750°C was $k \sim 2000$ and $\tan\delta \sim 0.05$, respectively.

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